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(54) **Network connection system.**

(57) One of the fundamental benefits of the 10BASE-T physical interface for 802-3/Ethernet is that the health of the communications link can be permanently monitored. When in the "link good" condition, the 10BASE-T transceiver is required to output a link status (LNKST) signal to this effect. Media Access Controller (MAC) 30, with an embedded 10BASE-T transceiver (37), uses the LNKST signal to provide power management to the MAC (30). By using the programmable AWAKE bit, the receive section of the 10BASE-T transceiver (37) can remain powered, even if the SLEEP input to the MAC (30) is activated. This allows the transceiver (37) to detect a link beat pulse (60) or receive packet activity. If either receive condition is encountered, the internal transceiver (37) will activate the LNKST output from the MAC (30). The system hardware and/or software can use the LNKST output to take appropriate action. For instance, if the LNKST output is active (low), then the computer is connected to an active network, and it is likely that the operating system will allow the MAC (30) to remain powered. However, if the LNKST becomes inactive (high) then the system can assume that the link is inactive, and the MAC can be powered down to save power. If at a later time the link is re-established, the MAC (30) can be powered back up to take advantage of the communications channel. In this way, the power consumption of the Ethernet connection can be managed by the operating software/hardware.

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The present invention relates generally to a system and method for automatic connection and disconnection of a node in a local area network (LAN) to manage power of a LAN controller. Preferably, it relates to such a system and method as implemented with an Ethernet network employing a transmitter/receiver (transceiver) which meets the ANSI/IEEE 802.3 10BASE-T international standard. Most preferably, it relates to such a system and method implemented in a media access controller (MAC) for 802.3/Ethernet, also known as MACE, implemented in an integrated circuit including the 10BASE-T transceiver.

Ethernet is a commonly used local area network scheme in which multiple stations are connected to a single shared serial data path. Typically, only one station can transmit data onto the path at a time. A station connected to the path transmits data in the form of a packet that includes a destination address. The packet propagates throughout the network medium and is received by all other stations. The addressed station copies the entire packet as it goes by; the others reject the packet after determining that it is addressed to another station.

A Media Access Controller (MAC) serves as an interface between a shared data path and the stations connected to that path. Each node connected to the network includes a MAC which performs a number of functions involved in the transmission and reception of data packets. For example, during the transmission of data, the MAC assembles the data to be transmitted into a packet with address and error detection fields. Conversely, during the reception of a packet, the MAC disassembles the packet and performs address checking and error detection. In addition, the MAC typically performs encoding/decoding of digital signals transmitted over the shared path and performs preamble generation/removal as well as bit transmission/reception.

Traditional Ethernet is a coaxial wired system. The coaxial cable provides a linear bus to which all nodes are connected. Signalling is accomplished using a current sink technique with the center conductor used for signal, and the shield as a ground reference.

Twisted pair Ethernet in accordance with IEEE Standard 802.3 10BASE-T is able to use standard voice grade telephone cable employing separate transmit and receive pairs (4 wires). The system uses a star topology. At the center of the star is a "repeater". The repeater (or hub) performs signal amplitude and timing restoration. It takes an incoming bit stream and repeats it to all other ports connected to it (but not back to the originating port). In this sense, the repeater acts as "logical coax", so that any node connected to the network will see another's transmission. Differential signalling is employed with one pair acting as the transmit path, and the other as receive. Further details on 10BASE-T Ethernet networks, including a description of the state of the art for such networks, is provided by Crayford, "10BASE-T in the Office," Wescon, San Francisco, November 20, 1991, the disclosure of which is hereby incorporated by reference herein.

Transceivers as used in 10BASE-T Ethernet networks may incorporate a sleep mode, in which a node incorporating the transceiver powers down when there is no activity for it after a predetermined period of time. However, when the node is in the sleep mode, it will miss any transmissions intended for it. It would be desirable to provide the power saving advantages of the sleep mode without resulting in missed transmissions. Efficient network management also requires that nodes on a network be identified as present. With prior art sleep modes, a network controller of a node in sleep mode is treated as if it were disconnected from the network.

We will describe a system and method in which a data processing network controller will operate in a sleep mode to conserve power, but in which a portion of the controller remains active to monitor for transmissions.

We will describe a system and method in which power consumption of a network connection is managed by operating software/hardware.

We will describe a system and method in which a data processing network controller is used to power down a terminal on a network from a central network system and in which the terminal can also be powered up from the central network system.

We will describe a system and method in which a portion of the network controller remains active to signal its presence to the network during the sleep mode.

The present invention provides a network connection system comprising a network controller having a sleep mode and a transceiver for connecting said network controller to a data processing network, said network controller being configured for allowing a sleep mode for a data processing node connected to said network controller, said transceiver being configured to monitor for transmissions on the data processing network when the data processing node is in the sleep mode.

The attainment of the foregoing and related advantages and features of the invention should be more readily apparent to those skilled in the art, after review of the following more detailed description of the invention, taken together with the drawings, in which:

BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 is a block diagram of a media access controller incorporating the invention for an Ethernet net-

work.

Figure 2 is a more detailed block diagram of a portion of the media access controller incorporating the invention, connected in a network.

Figure 3 is a block diagram corresponding to Figure 2 of an alternative embodiment of the invention, connected in a network.

Figure 4 is a diagram showing control bit assignments for practice of the invention.

Figures 5A and 5B are a more detailed block diagram of a portion of the media access controller of Figure 1.

DETAILED DESCRIPTION OF THE INVENTION

Turning now to the drawings, more particularly to Figure 1, there is shown a media access controller (MAC) 30 used as an interface in an Ethernet data processing network. The MAC 30 controls the passage of information between a shared Ethernet serial data path 32 and a system bus 34. The MAC 30 includes a Manchester encoder/decoder 36 and a MAC core 38, which in the preferred embodiment, is implemented in accordance with the IEEE 802.3 standard. The MAC 30 also includes a receive FIFO 40, a transmit FIFO 42 and FIFO control logic 44 as well as command and control registers 46. A system bus interface unit 48 is logically disposed between the receive and transmit FIFOs 40 and 42 and the system bus 34. The MAC core 38 includes a station address detect (SAD) unit 50 which determines whether packets received by the MAC 30 in fact are addressed to it and should be captured in their entirety, or are addressed to a different MAC station (not shown) and should be rejected. The byte tracking system of this invention is incorporated in the system bus interface unit 48, transmit FIFO 42, MAC core 38 and Manchester encoder/decoder 36 of the MAC 30. The encoder/decoder 36 is connected to an Attachment Unit Interface (AUI) transceiver 35 and a 10BASE-T transceiver 37. Further background information on the Ethernet data processing network and the MAC 30 are provided in commonly owned Crayford, Application Serial No. 07/841,113, filed February 24, 1992 and entitled, "Ethernet Media Access Controller with External Address Detection Interface and Associated Method," the disclosure of which is incorporated by reference herein.

Further details on the 10BASE-T transceiver 37, its connection to the MAC 30 and its connection to another 10BASE-T transceiver 37a are shown in Figure 2. In operation, the 10BASE-T transceiver 37 outputs a link beat pulse 60 every 8-24 milliseconds. Detection of the link beat pulses 60 produced by the transceiver 37 by the transceiver 37a is used to establish that a link in the network is in place.

Figure 3 shows a portion of another embodiment of a network system in which 10BASE-T transceivers 37' and 37a' each output a link beat pulse 60 every 8-24 milliseconds. Detection of the link beat pulses 60 produced by the other transceiver to which the transceivers 37' and 37a' are connected is used to establish that a link in the network is in place.

The "AUTO WAKE" (AWAKE) feature of the MAC 30 is specifically designed for use with mobile computing equipment, such as laptop or palmtop computers. The "REMOTE WAKE" (RWAKE) feature is intended for use in secure environments, providing a remote manager the capability of enabling or disabling a LAN station remotely, using a network command.

One of the fundamental benefits of the 10BASE-T physical interface for 802.3/Ethernet of Figure 2 is that the health of the communications link can be permanently monitored. This is known as the Link Integrity Test. When in the "link good" condition, the 10BASE-T transceiver 37 is required to output a link status (LNKST) signal to this effect. The MAC 30, with the embedded 10BASE-T transceiver 37, uses the LNKST signal to provide power management to the MAC 30.

A SLEEP pin (not shown) on the MAC 30 is an input that allows the MAC 30 to be placed in one of its power saving modes. When SLEEP is active, all outputs will be placed in an inactive or high impedance state, providing that both the AWAKE bit and the RWAKE bit functions in a PHY Configuration Control register (not shown) are not enabled. This is the optimal mode for power saving, allowing clock inputs to the MAC 30 to be suspended. If the AWAKE bit is set prior to the activation of SLEEP, the 10BASE-T receiver 37 and the LNKST output pin remain operational. If the RWAKE bit is set prior to SLEEP being asserted, the Manchester encoder/decoder 36, AUI cell and 10BASE-T cell 37 remain operational, as do the SRD, SRDCLK and SF/BD outputs. The input on XTALI must remain active for the AWAKE or RWAKE features to operate.

By using the programmable AWAKE bit, the receive section of the 10-BASE T transceiver 37 can remain powered, even if the SLEEP input to the MAC 30 is activated. This allows the transceiver 37 to detect a link beat pulse 60 or receive packet activity. If either receive condition is encountered, the internal transceiver 37 will activate the LNKST output from the MAC 30. The system hardware and/or software can use the LNKST output to take appropriate action. For instance, if the LNKST output is active (low), then the computer is connected to an active network, and it is likely that the operating system will allow the MAC 30 to remain powered.

However, if the LNKST becomes inactive (high) then the system can assume that the link is inactive, and the MAC can be powered down to save power. If at a later time the link is re-established, the MAC 30 can be powered back up to take advantage of the communications channel. In this way, the power consumption of the Ethernet connection can be managed by the operating software/hardware.

By using the programmable RWAKE bit, the receive section of the 10BASE-T transceiver 37 and AUI port remain powered after the SLEEP input to the MAC 30 is activated. In addition, External Address Detection Interface (EADI) 52 is maintained operational. This allows data received on either the AUI or 10BASE-T receiver 37 to be passed to the EADI 52, via the Manchester decoder 36, and hence allows an external address or message detection circuit to observe the contents of the incoming packet. In this way, a system can be simply designed to provide a central manager station with the ability to control remotely the state of the MAC 30 and the associated computer station.

External circuitry can be implemented to interface to pins of the EADI 52, and effectively wait for a match based on a specific receive frame contents, in addition to just address alone. By way of example, a serial to parallel converter 53 and a comparator 55 of a terminal to which the MAC 30 is connected are shown. The comparator 55 provides a remote wake signal in response to a command from the central manager to reawaken from a sleep mode. This mode of operation effectively allows the MAC 30 and associated external logic to respond to specific frames on the network and thereby facilitate remote controlled node operation.

Some emerging network management protocols, such as the 3Com/IBM HLM Specification, already have the requirement to be able to control the node remotely. For applications requiring network controlled power or security management, the EADI 52 of the MAC 30 offers a unique solution. Not only can the station be remotely powered down, but with segregated power supply distribution, the MAC 30 and the associated EADI logic can remain active, and effectively wait for a command to reactivate the station. This function is referred to as the "Remote Wake" capability. Since the logic for "snooping" on the receive packet, and the power management distribution, is under the control of the system designer, the end station can implement security as simply as disabling the keyboard entry and/or display system, or a more sophisticated power down of all non-critical functions, such as the system CPU itself.

The AWAKE and RWAKE bits are located in the PHY Configuration Control (PHYCC) register of the MAC 30. All bits within the PHY Configuration Control register are cleared by hardware or software reset. Bit assignments are as shown in Figure 4. Each bit has the function described in the following table:

Bit 7 LNKFL	LNKFL reports the link integrity of the 10BASE-T receiver. When the link test function is enabled (DLNKTST = 0), the absence of link beat pulses on the Receive Data (RXD)± pair will cause the integrated 10BASE-T transceiver 37 to go into the link fail state. In the link fail state, data transmission, data reception, data loopback and the collision detection functions are disabled, and remain disabled until valid data or >5 consecutive link pulses appear on the RXD± pair. During link fail, the LNKST pin is internally pulled HIGH. When the link is identified as functional, the LNKST pin is driven LOW, and is capable of directly driving a "Link OK" LED. In order to inter-operate with systems which do not implement Link Test, this function can be disabled by setting the DLNKTST bit. With Link Test disabled (DLNKTST = 1), the data driver, receiver and loopback functions as well as collision detection remain enabled irrespective of the presence or absence of data or link pulses on the RXD± pair. The transmitter will continue to generate link beat pulses during periods of transmit data inactivity.
Bit 6 DLNKTST	Disable Link Test. When set, the integrated 10BASE-T transceiver 37 will be forced into the link pass state, regardless of receive link test pulses or receive packet activity.
Bit 5 REVPOL	Reversed Polarity. Indicates the receive polarity of the RXD± pair. When normal polarity is detected, the REVPOL bit will be cleared, and the RXPOL pin (capable of driving a "Polarity OK" LED) will be driven low. When reverse polarity is detected, the REVPOL bit will be set, and the RXPOL pin should be externally pulled high.
Bit 4 DAPC	Disable Auto Polarity Correction. When set, the automatic polarity correction will be disabled. Polarity detection and indication will still be possible via the RXPOL pin.
Bit 3 LTS	Low Threshold Select. When set, the threshold of the twisted pair receiver will be reduced by 4.5 dB, to allow extended distance operation.
Bit 2 ASEL	Auto Select. When set, the PORTSEL [1-0] bits are overridden, and the MAC 30 will automatically select the operating media interface port. When the 10BASE-T transceiver is in the link pass state (due to receiving valid packet data and/or Link Test pulses or the DLNKTST bit is set), the 10BASE-T port will be used. When the 10BASE-T port is in the link fail state, the AUI port will be used. Switching between the ports will not occur during transmission, to avoid any type of fragment generation.

Bit 1 RWAKE Remote Wake. When set prior to the SLEEP pin being activated, the AUI and 10BASE-T receiver sections and the EADI 52 will continue to operate even during SLEEP. Incoming packet activity will be passed to the EADI 52 pins to allow detection of specific frame contents to be used to initiate removal of the SLEEP function. RWAKE must be programmed prior to SLEEP being asserted for this function to operate. RWAKE is not cleared by SLEEP, only by activation of the SWRST bit or RESET pin.

Bit 0 AWAKE Auto Wake. When set prior to the SLEEP pin being activated, the 10BASE-T receiver section will continue to operate even during SLEEP, and will activate the LNKST pin if receive activity is detected. AWAKE must be programmed prior to SLEEP being asserted for this function to operate. AWAKE is not cleared by SLEEP, only by activation of the SWRST bit or RESET pin.

Further details of the MAC 30 as shown in Figure 1 are provided in Figures 5A and 5B. These diagrams are useful for understanding operation of the AUI and 10BASE-T circuits 35 and 37, and their interaction with the SLEEP pin and AWAKE/RWAKE bits. The MAC core cell 38 performs all of the 802.3/Ethernet transmit and frame assembly and interfaces through the bus interface unit 48 to the host system bus 34. Port multiplexer 80 allows the MAC 30 to select between the AUI interface 35 and the 10BASE-T interface 37. The port selection process as such is not a part of the present invention and is therefore not described in detail, but it basically allows either manual selection (via programmable bits) or automatic selection (using the LNKST pin indication) of the media interface. Manchester encoder 82 and phase locked loop (PLL) and decoder 84 together form the Manchester encoder/decoder 36.

Assuming normal (SLEEP inactive) operation, transmit data flow originates from the MAC 38, which is Manchester encoded at 82 and then passed to the appropriate AUI or 10BASE-T media driver 86 or 88 through transmit logic 87 or 89. Receive data flow may originate from either the AUI or the 10BASE-T receiver 90 or 92. The receivers 90 and 92 first perform pulse amplitude detection. If the received waveform passes the amplitude criteria, pulse width detection is then performed at 94 or 96. In the case of the AUI, if pulse amplitude and width criteria are met, the signal is effectively qualified and passed to receive multiplexer 98 as AUI receive data and AUI receive carrier sense signals. Similarly, the AUI waveform is also received and amplitude tested by collision detection receiver 91 and tested for pulse width at 95. If the AUI waveform meets the amplitude and width criteria, an AUI collision signal is supplied to the port multiplexer to block AUI transmissions while an AUI waveform is being received. For the 10BASE-T receive path, data is further qualified by "smart squelch" 100, which in essence looks for multiple pulses in a specific order, to ascertain that this receive waveform is packet data and not noise. If the waveform passes this criteria, it is qualified and passed to the receive multiplexer 98 as 10BASE-T data and 10BASE-T receive carrier sense. The receive multiplexer 98 is set up to select one of the receive data paths (either by programming or automatically as previously discussed), allowing the receive data to be decoded from Manchester to non-return to zero (NRZ) format by the PLL/decoder 84, and passed to the MAC 38 through the port multiplexer 80.

In addition, for the 10BASE-T port, the transmission of link test pulses 60 (Figure 3) on line 101 is controlled by 10BASE-T transmit link state machine 102, so that pulses are transmitted during periods of data (packet) inactivity. 10BASE-T receive link state machine 104 monitors the presence of receive link beat pulses or packet data, and asserts the LNKST pin if the link is considered operable.

SLEEP 1 and SLEEP 2 outputs from port multiplexer 80 are effectively set up by the condition of the AWAKE and RWAKE bits. If the SLEEP pin is activated, and AWAKE and RWAKE have not been programmed, SLEEP 1 and SLEEP 2 will be activated, shutting down the AUI and 10BASE-T transmit and receive functions. Note that 10BASE-T link test pulses will not be generated by the MAC 30 when the SLEEP pin is asserted, as an additional power saving feature. Note also that the entire MAC core portion 38 of the chip will be in a low power sleep mode, consuming minimal power.

If AWAKE is set and the SLEEP pin activated, SLEEP 1 is asserted, but SLEEP 2 is not. This powers down the entire AUI transmit and receive paths, and the 10BASE-T transmit path. However, the 10BASE-T receiver 92 remains powered, and the receive chain remains operable, including the ability to detect link test pulses.

If RWAKE is set and the SLEEP pin activated, SLEEP 1 and SLEEP 2 are not asserted. Both the AUI and 10BASE-T receivers 90 and 92 remain operable, as does the PLL/decoder 84. Data received will be passed to the port multiplexer 80, which will allow the EADI 52 output pins Serial Receive Data (SRD), Serial Receive Data Clock (SRDCLK) and Start Frame/Byte Delimiter (SF/BD) to be driven. The transmit portions of the AUI and 10BASE-T interfaces will not be in their most optimal power conservation mode, but will be static (no data and/or link test pulses will be transmitted). The MAC core 38 will of course be in its power saving mode. Since this mode is intended for "security" applications, the power conservation issue is not deemed as important. Note that the LNKST pin output buffer is active when in the normal operating mode or when AWAKE is set before SLEEP is asserted (it is not active during RWAKE operation).

It should now be readily apparent to those skilled in the art that a novel network connection system and method for operating a data processing network capable of achieving the stated objects of the invention has been provided. The data processing network controller operates in a sleep mode to conserve power, but a portion of the controller remains active to monitor for transmissions. Power consumption of a network connection is managed by operating software/hardware. A data processing network controller can be used to power down a terminal on a network from a central network system, and the terminal can also be powered up from the central network system. A portion of the network controller remains active to signal its presence to the network during the sleep mode.

It should further be apparent to those skilled in the art that various changes in form and details of the invention as shown and described may be made. It is intended that such changes be included within the spirit and scope of the claims appended hereto.

Claims

1. A network connection system comprising a network controller having a sleep mode and a transceiver for connecting said network controller to a data processing network, said network controller being configured for allowing a sleep mode for a data processing node connected to said network controller, said transceiver being configured to monitor for transmissions on the data processing network when the data processing node is in the sleep mode.
2. The network connection system of Claim 1 in which said transceiver is further configured to initiate awakening of said network controller when a transmission on the data processing network intended for said data processing node is observed.
3. A network connection system comprising a network controller having a sleep mode and a transceiver for connecting said network controller to a data processing network, said network controller being configured for allowing a sleep mode for a data processing node connected to said network controller, the data processing network being configured to signal its presence to said transceiver when the data processing node is in the sleep mode.
4. The network connection system of Claim 3 in which said transceiver is further configured to signal its presence to said data processing network when the data processing node is in the sleep mode.
5. A data processing network system comprising the network connection system of Claim 3 with a data processing network connected to said network connection system, a data processing node connected in said data processing network through said network connection system, said data processing node including means for evaluating transmissions received by said network connection system when said data processing node is in the sleep mode.
6. The data processing network system of Claim 5 in which the transmissions include a node address and said means for evaluating transmissions is configured to determine if the node address of the transmissions identifies said data processing node.
7. The data processing network system of Claim 6 in which the transmissions further include an instruction field and said means for evaluating transmissions is further configured to determine if the instruction field of a transmission including a node address identifying said data processing node includes an awakening instruction for said data processing node.
8. A method for operating a data processing network, which comprises connecting a data processing node to a data processing network through a network controller and a transceiver, allowing the data processing node to assume a sleep mode, and monitoring the data processing network with the transceiver for transmissions when the data processing node is in the sleep mode.
9. The method of Claim 8 additionally comprising the step of periodically signalling the data processing network with the transceiver to signal presence of the transceiver on the data processing network.
10. A method for operating a data processing network, which comprises connecting a data processing node to a data processing network through a network controller and a transceiver, allowing the data processing

node to assume a sleep mode, and signalling the transceiver with the data processing network to signal presence of the data processing network when the data processing node is in the sleep mode.

- 5 **11.** The method of Claim 10 additionally comprising the step of periodically signalling the data processing network with the transceiver to signal presence of the transceiver on the data processing network.
- 10 **12.** The method of Claim 10 additionally comprising the steps of transmitting transmissions on the network to the data processing node through the transceiver and monitoring the transmissions by the data processing node.
- 15 **13.** The method of Claim 12 in which the transmissions include an address field and the data processing node monitors the address field to determine if the address field includes an address identifying the data processing node.
- 20 **14.** The method of Claim 13 in which the transmissions include an instruction field and the data processing node monitors the instruction field to determine if the instruction field includes an awakening instruction for the data processing node.
- 25 **15.** The method of Claim 8 additionally comprising the step of initiating awakening of the network controller when a transmission on the data processing network intended for the data processing node is observed.
- 30
- 35
- 40
- 45
- 50
- 55

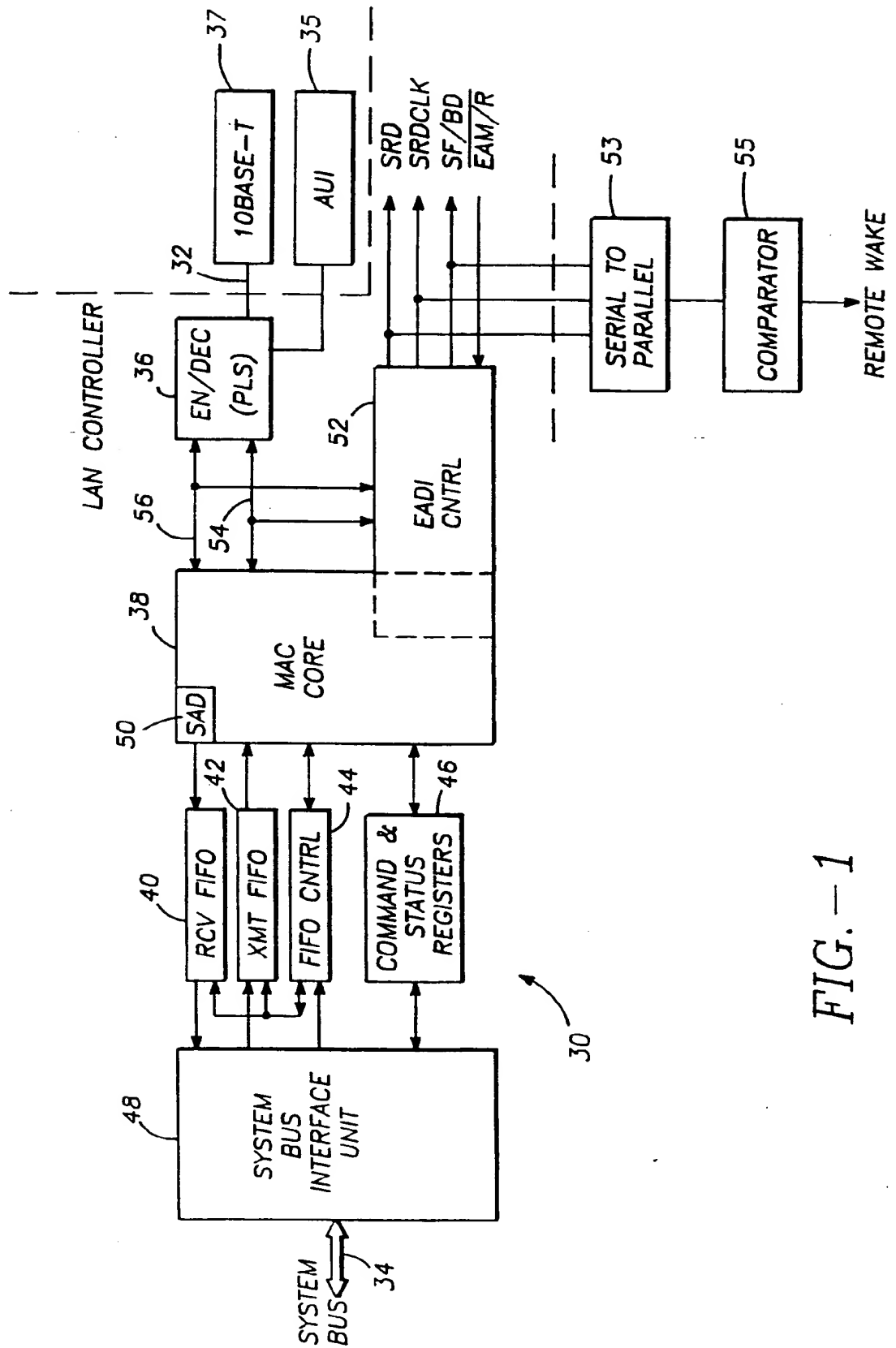


FIG. - 1

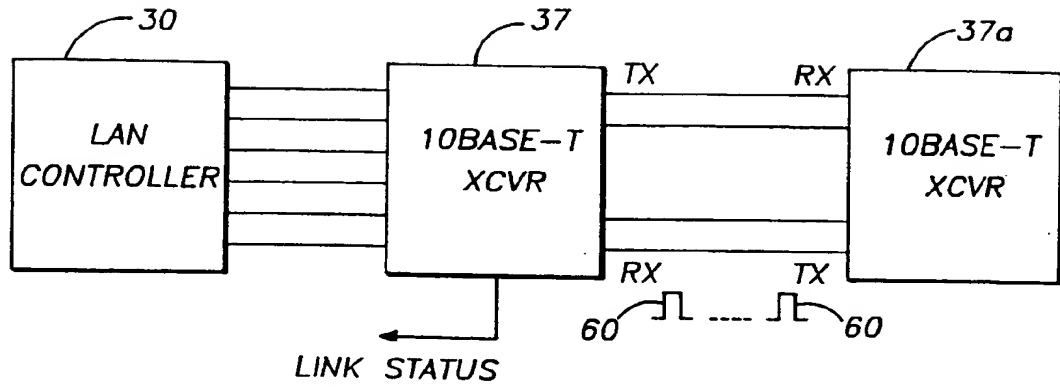


FIG. -2

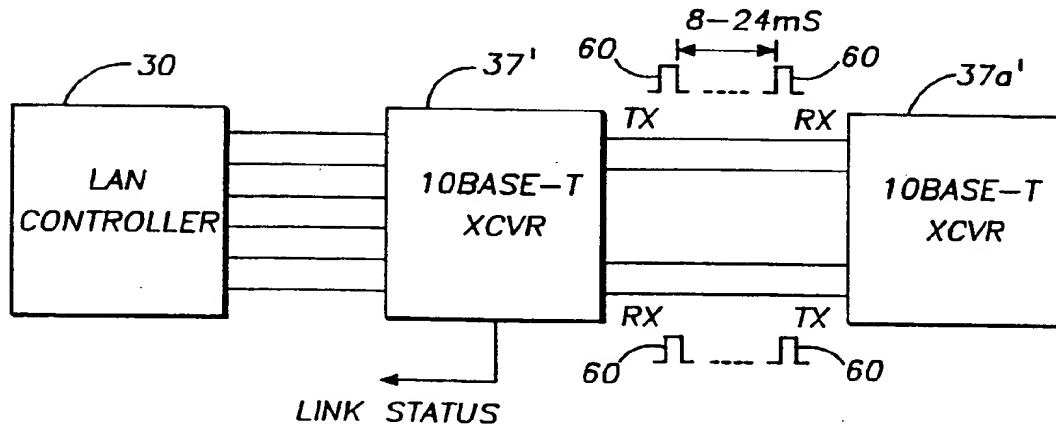


FIG. -3

LNKFL	DLNKTST	REVPOL	DAPC	LRT	ASEL	RWAKE	AWAKE
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FIG. -4

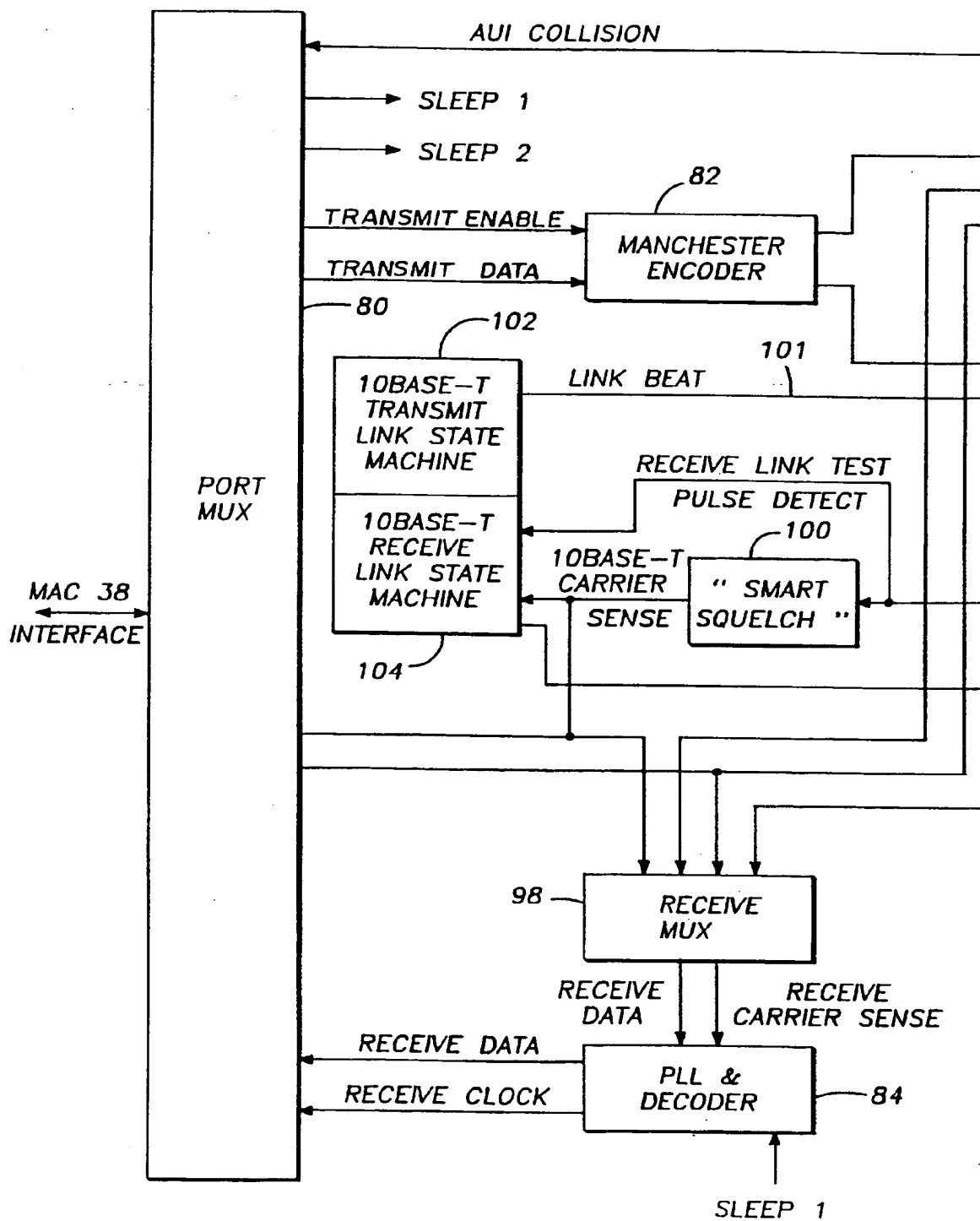


FIG. -5A

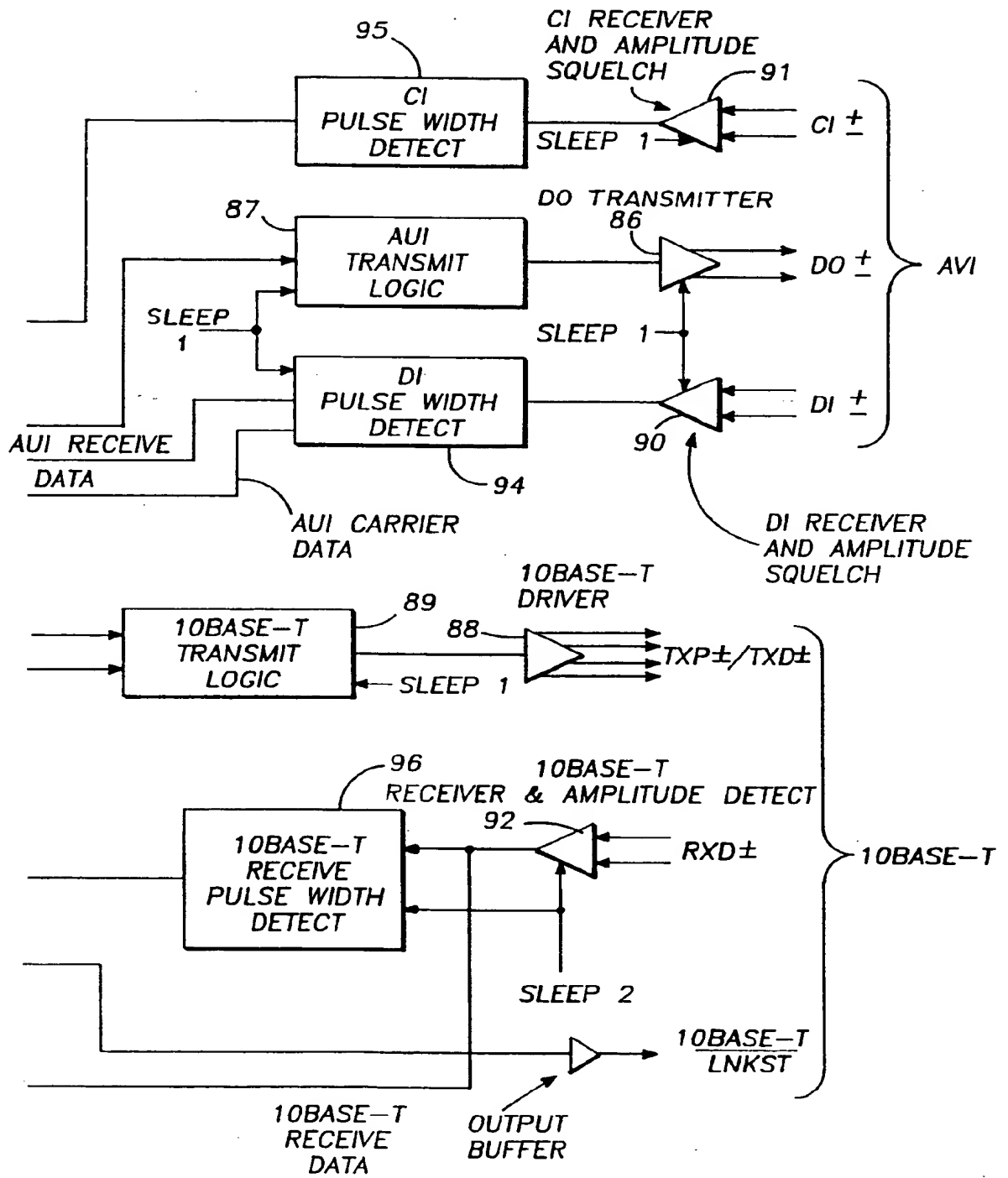


FIG.-5B



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(54) Network connection system.

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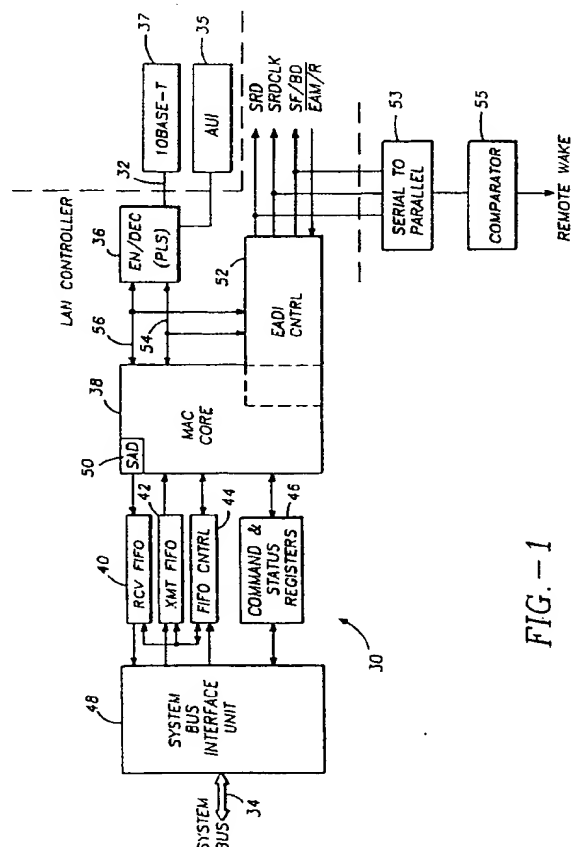


FIG - 1



European Patent
Office

EUROPEAN SEARCH REPORT

Application Number
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DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl. 5)
Y	WO-A-92 03004 (PULIZZI ENGINEERING, INC.) * page 3, line 30 - page 4, line 29 * * page 8, line 12 - line 33 * * figures 1,2 *	1-3,8, 10,12,15	H04L12/40
Y	US-A-4 996 706 (TAE-YOUNG CHO) * column 2, line 48 - column 3, line 5; figure 1A *	1-3,8, 10,12,15	
A	EP-A-0 343 630 (FUJITSU LIMITED) * column 5, line 21 - line 37 * * column 7, line 33 - line 49 * * column 8, line 37 - column 9, line 38 * * figures 3-6 *	6,7,13, 14	
A	US-A-4 903 016 (TOSHIHARU MURAI) * abstract; figures 1,2 *	6,13	
			TECHNICAL FIELDS SEARCHED (Int. Cl. 5)
			H04L
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 1 August 1994	Examiner D/L PINTA BALLE..., L
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document</p>			

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